

Using Flash and Shape Symbols in PCB Footprints

## **Using Flash and Shape Symbols in PCB Footprints**

This app note will go through the process of using shape and flash symbols in a PCB Footprint. The main reason these are used is because many modern PCB Footprints require non default style pad shapes for copper, soldermask and pastemask openings. Cadence Padstack Editor has been greatly enhanced to include a variety of different pad shapes (shown below) but there are still many instances where more complex style shapes are required.



This app note will focus on the flash style symbols and also how to resolve any issues you may have using either shape or flash symbols in the Cadence PCB tools.

## **Example Texas Instruments TCAN4550.**

Below is a screenshot of the datasheet for the Texas Instrument part TCAN4550. It has different definitions required for copper, mask and paste openings on the PCB Footprint.







This app note is not going to go through the process of how to generate the shape and flash symbols. If you need assistance on those steps please refer to this app note <u>How to Create an alternative shaped pad/pin (parallel-systems.co.uk)</u> which describes that process in detail. The key difference to remember is that a shape symbol can only contain one static shape, a flash symbol can contain multiple static shapes. Both MUST be drawn on the ETCH/Top subclass and should be saved with their origins in the centre of the shape which will coincide with the padstack origin point.

Once you have your shape and flash symbols defined using PCB Editor, launch Padstack Editor from the windows start menu found under the Cadence PCB Utilities VXX.XX menu. Use File – New and specify a name and padstack usage. In this example it is a SMD Pin. The units have also been defined as Millimeters.



We don't need to define any drill settings (SMD Pin type) so move straight to the Design Layers tab. Specify the pad geometry as Shape Symbol and browse for the shape symbol you have generated for the copper tab pad. The browse button will show entries from the psmpath location so make sure you save the filename.dra and filename.ssm or filename.fsm in this location.

Padstack Editor: test (C:/Library/D	ownloaded_Parts)							_		$\times$
File View Help										cādence
2D Top Padstack 🗗 🗙 Start Drill	Secondary Drill Drill	Symbol Drill Offset	Design Layers	Mask Layer	s Options	Summary				
☐ Select pa	d to change									
	Layer Name	Regular Pad	Thermal Pad	Anti Pad	Keep Out					
	BEGIN LAYER 🔻 Syr	nbol tcan4550_tab_pad	None	None	None					
	ADJACENT LAYER				None					
Regular F	Pad on layer BEGIN LA	YER							<mark>nn</mark> i	
Geometr	ry: Shape symbol						▼			
2D Padstack Side 🗗 🗙 Shape sy	ymbol: tcan4550_tab_	_pad								
Flash na	me:									
Width:										
Height:										
Offset x:	0.0000									
Offset y:	0.0000									
Side Front										
SMD Pin Units: Millimeter V Dec	imal places: 4 🔻									

Open the Mask Layers tab and repeat the steps for the copper pad but this time use the shape symbol generated for the soldermask opening.

Padstack Editor: test (C:/Library/Downlo	paded_Parts)		2-sl	– 🗆 X
File View Help				cādence'
2D Top Padstack 🗗 🗙 Start Drill Secon	ndary Drill Drill Symbol Drill Offset	Design Layers Mask Layers Options Summary		
☐ Select pad to cł	nange			
Layer N	ame Pad			Add Layer
SOLDERMASK	C_TOP Symbol tcan4550_tab_mas	k		
SOLDERMASK	C_BOTTOM None			
PASTEMASK_1	rOP None			
	None			
Pad on layer SC	DLDERMASK_TOP			
Geometry:	Shape symbol		<b>_</b>	
2D Padstack Side & X Shape symbol:	tcan4550_tab_mask			
Flash symbol:				
Width:	2.3400			
Height:	5.0400			
Offset x:	0.0000			
Offset y:	0.0000			
Side Front				
SMD Pin Units: Millimeter   Decimal p	laces: 4 🔻			

Now for the Pastemask opening we want to use a flash symbol. Select the pastemask\_top entry and change the geometry to Flash. You can now browse for an existing Flash symbol or use the "Create New Flash Symbol" button to launch PCB Editor and generate one.



Once saved you can select the flash symbol and add it. The biggest advantage of using a flash symbol over a shape symbol is the ability to add multiple shapes to one symbol.

Padstack Editor: test (C:/Lib	orary/Download	ed_Parts)	- 🗆 X
File View Help			cādence'
2D Top Padstack 🗗 🗙 Start	t Drill Seconda	y Drill Drill Symbol Drill Offset Design Layers Mask Layers Options Summary	
_ S	elect pad to chan	ре	
	Layer Nam	e Pad	Add Layer
	SOLDERMASK_T	<sub>pp</sub> Symbol tcan4550_tab_mask	
	SOLDERMASK_B	DTTOM None	
	PASTEMASK_TOP	Flash tcan4550_tab_paste	
		None	
Pa	ad on layer PAST	MASK_TOP	
	Geometry: F	ash 🗸 🗸	
2D Padstack Side 🗗 🗙	Shape symbol:		
F	Flash symbol: to	an4550_tab_paste	
v	Width: 2	1600	
	Height: 4	9000	
	Offect vi		
C	Offset y: 0	0000	
Side Front			
SMD Pin Units: Millimeter 🔻	Decimal plac	s: 4 •	

Once complete, save the pad and place it in your footprint. The completed footprint is below.



### **Trouble shooting issues.**

The key part to remember about using shape and flash symbols as part of a PCB Footprint is that whenever any type of PCB Footprint is used in a PCB Editor board file any symbols (package, mechanical, shape, flash or format) as well as padstacks are cached in the design. This means that if you need to make any changes to those items, they will need to be refreshed in the board file to show the updates. This are different methods for this depending on whether you are in a Package symbol or a board file.

### **Package Symbol**

Use Tools – Update Symbols



Select any shape or flash symbols that you want to refresh / update. It is also recommended that you enable Ignore Fixed property then click on Refresh to update the symbols accordingly.

🚼 Update Modules — 🗆 🗙						
Select definitions to update:						
🔽 🖾 Shape and flash symbols						
TCAN4550_TAB_MASK						
Z 🔶 TCAN4550_TAB_PAD						
✓ ◆ TCAN4550_TAB_PASTE						
or enter a file containing a list of symbols:						
Update STEP mapping data only						
Keep design padstack names for symbol pins						
Update symbol padstacks from library						
Reset symbol text location and size						
Reset customizable drill data						
Reset pin escapes (fanouts)						
Ripup Etch						
☑ Ignore FIXED property Viewlog						
Refresh Close Help						

You may also need to refresh any changes to the Padstack definition. This can be done from Tools – Padstack – Refresh.



#### In a Board File

The steps are similar to a Package Symbol but in this case use Place – Update Symbols, enable the Package Symbol you require as well as any associated Shape and Flash Symbols. You can also enable Update symbol padstacks from library and Ignore FIXED property. Click on Refresh to update the required items.



#### When an Update Symbols doesn't show the required results

There are some instances where you may have an old version board file (usually designed in pre-14.0 versions) where the design is set to use Old Style Flash Symbols. The best and the easiest way to recognize any other "old style flash" boards is to open constraint modes, Setup – Constraint Modes – Design, then General and Look at the design options - Negative Plane Sliver Spacing, it will say "Negative plane sliver spacing not supported in old style flash databases".

🗌 Analysis Modes							
Design	Name	Value	On	Off	Batch		
Electrical	Mark All Constraints						
Physical			-		-		
Spacing	V General						
Same Net Spacing	Negative plane islands oversize	<not set=""></not>		~			
Assembly	Negative plane sliver spacing		-		-		
<ul> <li>Design for Fabrication</li> </ul>	Testpoint pad to component spacing	Negative plane sliver spacing: not supported in old style flash database.					
Outline	Testpoint location to component spacing	<not set=""></not>		$\checkmark$			
Mask	Testpoint under component			$\checkmark$			
Annular Ping	BB Via layer separation	<not set=""></not>					
	Pin to route keepout			$\checkmark$			
	Minimum metal to metal spacing	0.0000mm		$\checkmark$			
Copper Spacing	Duplicate drill hole			1			
Silkscreen	On-line InterLaver checks			J.			
Design for Assembly	Soldermask			1			
Outline							
PkgToPkg Spacing	Acute Angle Detection						
Spacing	Package			$\checkmark$			
Pastemask	SMD Pin						
Design for Test	Spacing Options						
	Mechanical Spacing						

If this is the case, you need to save the board file and close PCB Editor. Open a Windows DOS command prompt and browse to the location of the board file and run the flash\_convert utility over the board file.

Current versions of the Cadence SPB / OrCAD tools no longer rely on the Windows PATH Environment Variable to function so the flash\_convert utility will need the full path specified in the command prompt to run – it will be in the product installation folder, then tools\bin\flash\_convert. For example:-

# C:\Demos\SPB174\New-DemoP\allegro>C:\Cadence\SPB\_17.4\tools\bin\flash\_convert.exe steve.brd

FILE: steve.brd Performing a partial design check before saving. Writing design to disk. 'steve.brd' saved to disk.

C:\Demos\SPB174\New-DemoP\allegro>

After running the flash\_convert utility, the Flash Symbols should behave as expected.

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